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Application Number	09/741857
Filing Date	12/22/2000
First Named Inventor	Modelski
Art Unit	2631
Examiner Name	
Attorney Docket Number	120-100

## Sheet

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Attorney Docket Number 120-100

Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Ldt		Vibhatavanij, K. et al., "Simultaneous Multithreading-based Routers", Proceedings to the 2000 International Conf. on Parallel Processing; 21 Aug. 2000, pages 362-369	
Ldt		Eggers S.J. et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors" IEEE Micro, IEEE Inc. New York, US, Vol. 17, no. 5, 1 September 1997, pages 12-19	
Ldt		Chang A. et al., "The Effects of Explicitly Parallel Mechanisms on the Multi-ALU Processor Cluster Pipeline" Computer Design: VLSI In Computers and Processors, 1998, ICCD '98, Proceedings, International Conference on Austin TX, Oct 5-7, 1998	

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